

REMARKS

Claims 1-20 are pending in the present application.

Claims 1-20 have been rejected.

Claims 1-20 remain in the application. Reconsideration of the claims in view of the following arguments is respectfully requested.

In Sections 1 and 2 of the September 8, 2003 Office Action, the Examiner rejected independent Claims 1, 3, 9-11 and 17 under 35 U.S.C. §102(e) as being anticipated over U.S. Patent No. 5,889,816 to *Agrawal et al.* (hereafter, "*Agrawal*"). Among other things, the Examiner asserted that the *Agrawal* reference discloses all of the limitations recited in Claim 1, including that "the first state machine is capable of communication with a second state machine of the call control processor by storing at least one event with the second state machine." The Examiner asserted that this particular limitation was found in the *Agrawal* reference in Figure 6 and at column 8, lines 11-32. The Applicant respectfully traverses the rejection of Claims 1, 3, 9-11 and 17.

The Applicant directs the Examiner's attention to Claim 1, which contains following unique and novel limitations:

1. For use in a base station of a wireless network, a call control processor comprising:
a first state machine capable of performing a call processing task, said first state machine comprising a queue capable of storing a plurality of events associated with said call processing task, each of said plurality of events operable to cause said first state machine to perform a selected action, wherein said first state machine is capable of communicating with a second state machine of said call control processor by storing at least one event in a queue associated with said second state machine.

(emphasis added).

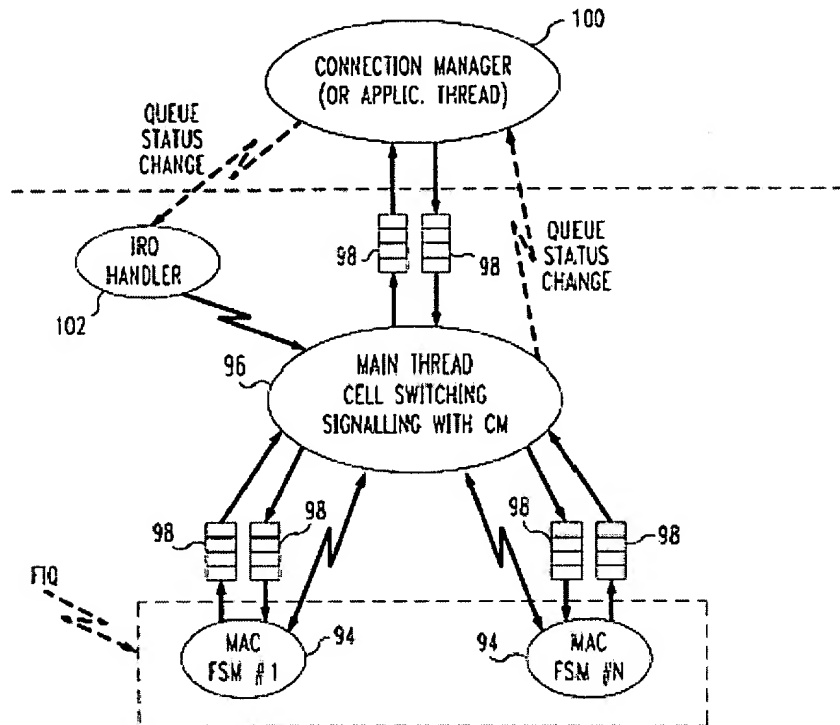
The Applicant respectfully asserts that the above-emphasized limitations are not disclosed, suggested, or even hinted at in the *Agrawal* reference.

The Examiner asserted that Figure 6 of the *Agrawal* reference and the accompanying text at column 8, lines 11-32 discloses that “the first state machine is capable of communication with a second state machine of the call control processor by storing at least one event with the second state machine.” The Applicant respectfully asserts that the Examiner has misdescribed the device disclosed in the *Agrawal* reference and misapplied it to the limitations recited in Claim 1 of the present application. Moreover, the Applicant respectfully asserts that, in fact, the *Agrawal* reference teaches away from Claim 1.

The Applicant notes that the Background section of the present application discussed at length the problems with state machines associated with prior art call processors. One of these problems was that call processing state machines did not communicate directly with each other. Rather, conventional call processing state machines communicated indirectly with each other through the operating system. To overcome this problem, the present invention discloses state machines that communicate directly with the internal queues of other state machines, thereby bypassing the operating system state machines. The Applicant respectfully asserts that, contrary to the Examiner’s description, the state machines 94 illustrated in Figure 6 and described at column 8, lines 11-32, actually communicate indirectly through main thread 96 (i.e., the operating system

thread) and not directly with each other.

Figure 6 of the *Agrawal* reference depicts the following:



Additionally, the text relied upon by the Examiner at column 8 lines, 11-32, of the *Agrawal* reference states:

The organization of the software embedded on the wireless adapter is shown in FIG. 6. The software is organized as a multi-threaded system. The finite state machines corresponding to the Medium Access Control protocol at each radio port are implemented as FSMs 94 running in the interrupt mode. There is one such FSM 94 for each radio port. These can be viewed as very high priority threads. The Medium Access Control FSMs 94 communicate with a main thread 96 that runs in the user mode and handles queue management and dispatching of ATM cells to the Medium

Access Control FSMs 94 on one side, and to other threads or to the base station/mobile unit CPU on the other side. The inter-thread communication is done using queues of pointers 98, with the ATM cells themselves being stored in a shared memory area. It is worth pointing out that in the case of dumb terminals with no CPU of their own, the ATM connection manager 100 and the threads that source or sink ATM cells are also run on the embedded CPU (an ARM610 processor) on the wireless adapter. An IRQ Handler 102 processes interrupt requests in response to a queue status change. [emphasis added]

The circuit block diagram in Figure 6 of the *Agrawal* reference clearly shows that data is written into and read from the queues 98 of each finite state machine (FSM) 94 only by the FSM 94 itself or by main thread 96. This is reinforced by the text of the *Agrawal* reference cited above.

In sum, Claim 1 of the present application contains unique and novel limitations that are not disclosed, suggested, or even hinted at in the *Agrawal* reference. To the contrary, the *Agrawal* reference teaches the opposite of what is recited in Claim 1. This being the case, Claim 1 is patentable over the *Agrawal* reference.

In Section 3 of the September 8, 2003 Office Action, the Examiner rejected Claims 6-8, 14-16, and 18-20 under 35 U.S.C. §103(a) as being unpatentable over the *Agrawal* reference in view of U.S. Patent No. 5,995,831 to *Gulliford et al.* (hereafter, "*Gulliford*"). In Section 4 of the September 8, 2003 Office Action, the Examiner rejected Claims 4, 5, 12, and 13 under 35 U.S.C. §103(a) as being unpatentable over the *Agrawal* reference in view of U.S. Patent No. 6,308,080 to *Burt et al.* (hereafter, "*Burt*").

The Applicant respectfully asserts that neither the *Gulliford* reference nor the *Burt* reference overcomes the shortcomings of the *Agrawal* reference regarding the unique and novel limitations

recited in Claim 1. The *Gulliford* reference purportedly introduces a telephone control system in which state machines respond to events based on an array and table which is a list linking events to responses. The *Burt* reference purportedly introduces a periodic ping message. However, the Examiner's assertions regarding the subject matter disclosed in the *Gulliford* reference and the *Burt* reference are irrelevant because neither the *Gulliford* reference nor the *Burt* reference discloses the unique and novel limitations described above in Claim 1.

This being the case, Claim 1 recites unique and novel limitations that are not disclosed, suggested, or even hinted at in the *Agrawal* reference, in the *Gulliford* reference, or in the *Burt* reference, or in any combination of the *Agrawal*, *Gulliford* and *Burt* references. Thus, Claim 1 presents patentable subject matter over the *Agrawal*, *Gulliford* and *Burt* references. Also, Claims 2-8 depend from Claim 1 and contain all of the unique and novel limitations recited in Claim 1. Therefore, Claims 2-8 also are patentable over the *Agrawal*, *Gulliford* and *Burt* references.

The Applicant respectfully asserts that independent Claims 9 and 17 contain limitations that are analogous to the unique and novel limitations recited in Claim 1. This being the case, independent Claims 9 and 17 present patentable subject matter over the *Agrawal*, *Gulliford* and *Burt* references. Finally, Claims 10-16, which depend from Claim 9, and Claims 18-20, which depend from Claim 17, contain all of the unique and novel limitations recited in Claims 9 and 17, respectively. Therefore, Claims 10-16 and 18-20 are patentable over the *Agrawal*, *Gulliford* and *Burt* references.

SUMMARY

For the reasons given above, the Applicant respectfully requests reconsideration and allowance of pending claims and that this Application be passed to issue. If any outstanding issues remain, or if the Examiner has any further suggestions for expediting allowance of this Application, the Applicant respectfully invites the Examiner to contact the undersigned at the telephone number indicated below or at *jmockler@davismunck.com*.

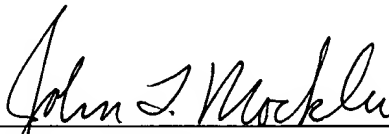
The Commissioner is hereby authorized to charge any additional fees connected with this communication or credit any overpayment to Deposit Account No. 50-0208.

Respectfully submitted,
DAVIS MUNCK, P.C.

Date:

31 Dec, 2003

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